

#2  
02/11/01  
11-22-01

Attorney Docket No. TT4314  
PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

Christopher L. Wooten  
Arturo Morosoff

Group Art Unit: Not Yet Assigned

Serial No.: Not Yet Assigned

Filed: Herewith

Title: METHOD FOR EVALUATING ANOMALIES IN A SEMICONDUCTOR  
MANUFACTURING PROCESS

JCE21 U.S. PRO  
09/976739  
10/11/01

**INFORMATION DISCLOSURE STATEMENT**

Assistant Commissioner for Patents  
Washington, DC 20231

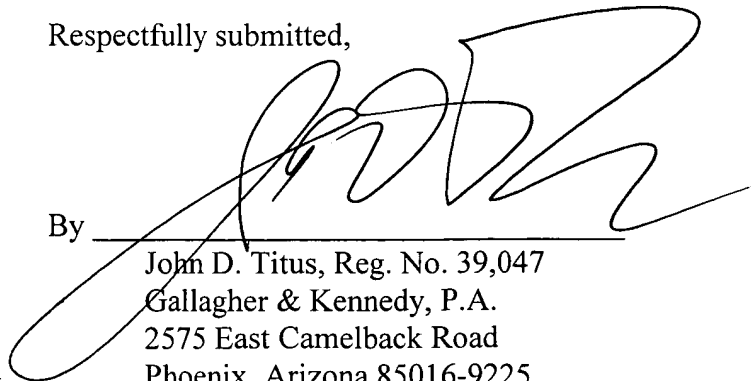
Sir:

Attached hereto is a completed Form PTO-1449A listing patents, publications, or other information which the applicant believes may be material to the examination of this application, with copies of each such item enclosed herewith. It is requested that the cited patents be made of record in the examination of this application.

Respectfully submitted,

Dated: 11 OCT 2001

By



John D. Titus, Reg. No. 39,047  
Gallagher & Kennedy, P.A.  
2575 East Camelback Road  
Phoenix, Arizona 85016-9225  
Telephone: (602) 530-8000